

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re: Application of:

JAE-BUM KO, ET AL.

Application No.: 10/749,900

Filed: December 30, 2003

For: **Semiconductor Memory Device Having
Tag Block for Reducing Initialization
Time**

Art Group: 2188

Examiner: Craig E. Walter

Confirmation No.: 1427

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted concurrently with the Request for Continued Examination. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).



The references were cited in a Search Report dated July 23, 2007 (copy enclosed hereewith) which was forwarded to Applicant's Representative in a communication dated August 16, 2007.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Date: _____

9/17/07

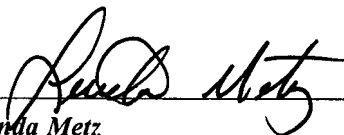
Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Linda Metz

9-17-07
Date